



4th International Conference on Machine Learning and Autonomous Systems (ICMLAS 2027)

25–27 February 2027

organized by



Technical Sponsors



About the Conference

The emerging modern mobile robotic systems and intelligent information systems are becoming increasingly ubiquitous and distributed, where they are intended to operate in highly dynamic and continuously fluctuating societal and economic environments. Moreover, the introduction of autonomous systems has revolutionized a wide range of applications like agriculture, healthcare, education, military, industries, and so on. The 4th International Conference on Machine Learning and Autonomous Systems [ICMLAS 2027] on 25–27 February 2027 will be an interdisciplinary platform for the researchers to contribute their research works that address the state-of-the-art technologies, development, and methodologies for the futuristic intelligent autonomous system developments and applications.

About the College–Stamford International University

Named after Sir Stamford Raffles (1781– 1826), Stamford International University opened its first campus in Thailand in 1995, near Palm Hills Golf Resort, Hua Hin. To meet the growing demand for international education in Thailand and the ASEAN region, Stamford opened its second campus in Bangkok in 2009, near Rama 9 road. In 2015, Stamford opened its downtown learning center, the Asoke Campus Learning Center, in the heart of Bangkok's business district.

Contact Us

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Submission

Submission Mode:

Authors can submit their full-paper [6-8 pages] via email: icmlasconference@gmail.com.

Earlier paper submission is encouraged as it helps us to manage the review process in a timely manner.

Manuscript Preparation Guidelines & Submission Template:

- Papers must not exceed 5000 words in length, including abstract, figures, references and appendices for a total of 6-8 pages.
- Before submitting your paper, kindly ensure that it has been carefully read for typographical and grammatical errors. Avoid using both British and American English in a paper. Papers will be returned if the standard of English is not considered to be good enough for publication.
- Figures should be designed with clear, large type and visible graphics. Each figure or table should have a heading of one or two lines.
- Papers should be submitted as a .doc or .pdf attachment by email to the conference chair.
- All papers received within the due date will be sent for peer review.

CALL FOR PAPERS

Track 1: Hardware Acceleration for Machine Learning

- ASIC Design for Neural Network Acceleration
- FPGA-Based Accelerators
- Quantum Computing for ML
- Memristor-based Neuromorphic Computing
- Optical Neural Networks
- Reconfigurable Computing
- Energy-Efficient Accelerator Design

Track 2: Sensor Integration and Data Acquisition

- Embedded Sensor Interfaces
- Edge Computing for Sensor Fusion
- Real-time Signal Processing
- Low-Power Data Acquisition Circuits
- Wireless Sensor Networks
- High-Speed Data Converters
- Biometric Sensor Integration

Track 3: Autonomous Control Systems

- Embedded Control Systems
- PID Controller Design
- Kalman Filtering for Sensor Fusion
- Model Predictive Control (MPC)
- Fault-Tolerant Control Circuits
- Neural Network Control Systems
- Real-time Operating Systems (RTOS)

Registration Fee

		IEEE EDS Member	IEEE Member	Non-IEEE Member
Author	Student	140 USD	160 USD	180 USD
	Faculty/Research Scholar/Industry Expert	150 USD	170 USD	190 USD
Listener		120 USD	130 USD	140 USD

PUBLICATION

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Accepted and presented conference papers will be recommended for inclusion in IEEE Xplore.

Author Ethics:

<https://conferences.ieeeauthorcenter.ieee.org/author-ethicsethical-requirements>

Keynote Speakers

Dr. R. Kanthavel

Papua New Guinea University of Technology (PNGUoT),
Papua New Guinea.

Dr. Shajulin Benedict

Guest Professor of TUM-Germany &
Faculty-InCharge (R&D and International Affairs)
Indian Institute of Information Technology,
Kottayam, India.

Dr. V. Suma

Vice Principal & Professor and Head,
Department of Computer Science and Design,
Dayananda Sagar College of Engineering,
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